AMENDMENT UNDER 37 C.F.R. 1.116 – EXPEDITED PROCEDURE

Serial Number: 10/647,396 Filing Date: August 25, 2003

Title: CONNECTOR ASSEMBLY WITH DECOUPLING CAPACITORS

Assignee: Intel Corporation

IN THE CLAIMS

Please amend the claims as follows.

- 1-18. (Cancelled)
- 19. (Currently Amended) A method of making a connector assembly, comprising:

 disposing a first conductive layer over a second conductive layer to define a cable,
 forming a cable receiver operable to attach a cable to the connector assembly, the cable
 including a first conductive layer disposed over a second conductive layer to define the cable,
 wherein the first conductive layer is insulated from the second conductive layer;

connecting the first conductive layer to a terminal of a connector plug comprising a part of the connector assembly;

connecting the second conductive layer to another terminal of the connector plug; and
forming a first terminal of a connector plug operable to attach to the first conductive layer
of the cable;

forming a second terminal of the connector plug operable to attach to the second conductive layer of the cable; and

connecting a plurality of capacitors between the first and second <u>terminals</u> as a part of within the connector assembly.

20. (Currently Amended) The method of claim 19, wherein connecting the plurality of capacitors comprises:

removing a portion of the insulation material from the first conductive layer of an attached cable within the connector assembly according to a predetermined pattern to expose at least a portion of the first conductive layer;

forming openings through the first conductive layer of the attached cable according to another predetermined pattern;

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removing a portion of the insulation material covering the second conductive layer of the attached cable according to the other predetermined pattern to expose at least a portion of the second conductive layer through the opening in the first conductive layer and insulation material; connecting one terminal of each capacitor to the exposed first conductive layer; and connecting another terminal of each capacitor to the exposed second conductive layer.

- 21. (Original) The method of claim 19, wherein the first conductive layer, the second conductive layer and the layer of insulation material are flexible.
- 22. (Original) The method of claim 19, further comprising coating the first and second conductive layers with mylar.
- 23. (Original) The method of claim 19, further comprising selecting a quantity of the plurality of capacitors and a size of each of the plurality of capacitors to provide a predetermined reduction in equivalent series resistance, voltage droop and settling time.
- 24. (Withdrawn) A method of making a testing system for an integrated circuit, comprising:

 forming a chassis for holding the integrated circuit; and

 forming a connector assembly for attaching a power supply to the integrated

 circuit, wherein forming the connector assembly includes:

disposing a first conductive layer over a second conductive layer to define a cable, wherein the first conductive layer is insulated from the second conductive layer,

connecting the first conductive layer to a terminal of a connector plug,
connecting the second conductive layer to another terminal of the
connector plug, and
connecting a plurality of capacitors between the first and second

conductive layers.

25. (Withdrawn) The method of claim 24, wherein connecting the plurality of capacitors comprises:

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removing a portion of the insulation material from the first conductive layer according to a predetermined pattern to expose at least a portion of the first conductive layer; forming openings through the first conductive layer according to another

predetermined pattern;

removing a portion of the insulation material covering the second conductive layer according to the other predetermined pattern to expose at least a portion of the second conductive layer through the opening in the first conductive layer and insulation material;

connecting one terminal of each capacitor to the exposed first conductive layer;

and

layer.

connecting another terminal of each capacitor to the exposed second conductive

26. (Withdrawn) The method of claim 24, further comprising:

forming a floating and self-aligning suspension system; and
attaching the connector assembly to the floating and self-aligning suspension
system.

27. (Withdrawn) The method of claim 24, wherein forming the floating and self-aligning suspension system comprises:

forming an inner frame;

attaching a biasing arrangement to the inner frame;

mounting the connector to the inner frame;

forming an outer frame to mount on the chassis; and

disposing the inner frame within the outer frame, wherein the biasing arrangement permits the inner frame to move relative to the outer frame to allow the connector to self-align and attach to a mating connector on the integrated circuit.

28. (Withdrawn) The method of claim 24, wherein the first conductive layer, the second conductive layer and the layer of insulation material are flexible.

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29. (Withdrawn) The method of claim 24, further comprising selecting a quantity of the plurality of capacitors and a size of each of the plurality of capacitors to provide a predetermined reduction in equivalent series resistance, voltage droop and settling time.

- 30. (New) The method of claim 19, further comprising formation of connection elements operable to couple the connector assembly to an integrated circuit.
- 31. (New) The method of claim 30, further comprising formation of a self-aligning suspension system operable to receive the integrated circuit.
- 32. (New) The method of claim 30, further comprising forming a frame element operable to support the integrated circuit.